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**\*\* CONTINUING DATA \*\*\*\*\*** *YES*

This appln claims benefit of 60/272,064 03/01/2001 *11/11/2005*

**\*\* FOREIGN APPLICATIONS \*\*\*\*\*** *NONE*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED \*\* SMALL ENTITY \*\*

\*\* 03/26/2002

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged <i>[Signature]</i> Examiner's Signature	CA	22	83	11

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## TITLE

Method and apparatus for diagnosing failures in an integrated circuit using design-for-debug (DFD) techniques